P240FZ4QNKA

Power MOSFETs
40V, 240A, N-channel

Feature

- N-channel
- SMD
- Super Large Current
- Low Ron
- 10V Gate Drive
- Low Capacitance
- Based on AEC-Q107
- Halogen free
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FZ-7p
Package (JEDEC Code): TO-263SC

Equivalent circuit

Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 175	°C
Channel tempertature	Tch		-55 to 175	°C
Drain-source voltage	V_{DSS}		40	V
Gate-source voltage	V_{GSS}		±20	V
Continuous drain current(DC)	I_D		240	A
Continuous drain current(Peak)	I_{DP}	Pulse width 10μs, duty=1/100	720	A
Total power dissipation	P_T		178	W
Single avalanche current	I_{AS}	Starting Tch=25°C Tch≦150°C	81	A
Single avalanche energy	E_{AS}	Starting Tch=25°C Tch≦150°C	328	mJ

※ :See the original Specifications

Electrical Characteristics (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	40			V
Zero gate voltage drain current	I_{DSS}	VDS=40V, VGS=0V			1	μA
Gate-source leakage current	I_{GSS}	VGS=±20V, VDS=0V			±0.1	μA
Forward transconductance	g_{fs}	ID=60A, VDS=10V	30			S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=120A, VGS=10V		0.00107	0.00134	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	2	3	4	V
Source-drain diode forward voltage	V_{SD}	IS=120A, VGS=0V			1.5	V
Thermal resistance	Rth(j-c)	Junction to case, with heatsink			0.84	°C/W
Total gate charge	Qg	VDD=32V, VGS=10V, ID=120A		133		nC
Gate to source charge	Qgs	VDD=32V, VGS=10V, ID=120A		38		nC
Gate to drain charge	Qgd	VDD=32V, VGS=10V, ID=120A		47		nC
Input capacitance	Ciss	VDS=25V, VGS=0V, f=1MHz		7915		pF
Reverce transfer capacitnce	Crss	VDS=25V, VGS=0V, f=1MHz		565		pF
Output capacitance	Coss	VDS=25V, VGS=0V, f=1MHz		1180		pF
Turn-on delay time	td(on)	ID=60A, RL=0.33Ω, VDD=20V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		14.5		ns
Rise time	tr	ID=60A, RL=0.33Ω, VDD=20V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		29		ns
Turn-off delay time	td(off)	ID=60A, RL=0.33Ω, VDD=20V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		85		ns
Fall time	tf	ID=60A, RL=0.33Ω, VDD=20V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		41		ns
Diode reverse recovery time	trr	IF=120A, VGS=0V, di/dt=100A/μs		48		ns
Diode reverse recovery charge	Qrr	IF=120A, VGS=0V, di/dt=100A/μs		61		nC

※ ：See the original Specifications

CHARACTERISTIC DIAGRAMS

Typical output characteristics

Transfer characteristics

Static drain-source on-state resistance vs drain current

Static drain-source on-state resistance vs case temperature

Gate threshold voltage
vs case temperature

Safe operating area

Transient thermal impedance

Capacitance characteristics

Power derating – case temperature

Gate charge characteristics

Single avalanche energy derating
vs channel temperature

Outline Dimensions

unit:mm

H7

JEDEC Code	TO-263SC
JEITA Code	–
House Name	FZ-7p

• Optimize soldering pad to the board design and soldering condition

Notes

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