

5SNG 0250P330305

HiPak IGBT Module

 $V_{CE} = 3300 \text{ V}$
 $I_C = 250 \text{ A}$

Ultra low-loss, rugged SPT+ chip-set
 Smooth switching SPT+ chip-set for good EMC
 AlSiC base-plate for high power cycling capability
 AlN substrate for low thermal resistance
 Recognized under UL1557, File E196689



Maximum rated values ¹⁾

Parameter	Symbol	Conditions	min	max	Unit
Collector-emitter voltage	V_{CES}	$V_{GE} = 0 \text{ V}$, $T_{vj} \geq 25 \text{ }^{\circ}\text{C}$		3300	V
DC collector current	I_C	$T_C = 100 \text{ }^{\circ}\text{C}$, $T_{vj} = 150 \text{ }^{\circ}\text{C}$		250	A
Peak collector current	I_{CM}	$t_p = 1 \text{ ms}$		500	A
Gate-emitter voltage	V_{GES}		-20	20	V
Total power dissipation	P_{tot}	$T_C = 25 \text{ }^{\circ}\text{C}$, $T_{vj} = 150 \text{ }^{\circ}\text{C}$		2450	W
DC forward current	I_F			250	A
Peak forward current	I_{FRM}	$t_p = 1 \text{ ms}$		500	A
Surge current	I_{FSM}	$V_R = 0 \text{ V}$, $T_{vj} = 150 \text{ }^{\circ}\text{C}$, $t_p = 10 \text{ ms}$, half-sinewave		2250	A
IGBT short circuit SOA	t_{psc}	$V_{CC} = 2500 \text{ V}$, $V_{CEM \text{ CHIP}} \leq 3300 \text{ V}$ $V_{GE} \leq 15 \text{ V}$, $T_{vj} \leq 150 \text{ }^{\circ}\text{C}$		10	μs
Isolation voltage	V_{isol}	1 min, $f = 50 \text{ Hz}$		6000	V
Junction temperature	T_{vj}			175	$^{\circ}\text{C}$
Junction operating temperature	$T_{vj(op)}$		-50	150	$^{\circ}\text{C}$
Case temperature	T_C		-50	125	$^{\circ}\text{C}$
Storage temperature	T_{stg}		-50	125	$^{\circ}\text{C}$
Mounting torques ²⁾	M_s	Base-heatsink, M6 screws	4	6	Nm
	M_{t1}	Main terminals, M6 screws	4	6	

¹⁾ Maximum rated values indicate limits beyond which damage to the device may occur per IEC 60747

²⁾ For detailed mounting instructions refer to Document No. 5SYA 2039

IGBT characteristic values ³⁾

Parameter	Symbol	Conditions	min	typ	max	Unit	
Collector (-emitter) breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0 \text{ V}$, $I_C = 10 \text{ mA}$, $T_{vj} = 25 \text{ }^{\circ}\text{C}$	3300			V	
Collector-emitter ⁴⁾ saturation voltage	$V_{CE \text{ sat}}$	$I_C = 250 \text{ A}$, $V_{GE} = 15 \text{ V}$	$T_{vj} = 25 \text{ }^{\circ}\text{C}$		2.5	2.9	V
			$T_{vj} = 125 \text{ }^{\circ}\text{C}$		3.1	3.4	V
			$T_{vj} = 150 \text{ }^{\circ}\text{C}$		3.25		V
Collector cut-off current	I_{CES}	$V_{CE} = 3300 \text{ V}$, $V_{GE} = 0 \text{ V}$	$T_{vj} = 25 \text{ }^{\circ}\text{C}$			0.17	mA
			$T_{vj} = 125 \text{ }^{\circ}\text{C}$		3.5	7	mA
			$T_{vj} = 150 \text{ }^{\circ}\text{C}$		17		mA
Gate leakage current	I_{GES}	$V_{CE} = 0 \text{ V}$, $V_{GE} = \pm 20 \text{ V}$, $T_{vj} = 125 \text{ }^{\circ}\text{C}$	-500		500	nA	
Gate-emitter threshold voltage	$V_{GE(TO)}$	$I_C = 40 \text{ mA}$, $V_{CE} = V_{GE}$, $T_{vj} = 25 \text{ }^{\circ}\text{C}$	5		7	V	
Gate charge	Q_{ge}	$I_C = 250 \text{ A}$, $V_{CE} = 1800 \text{ V}$, $V_{GE} = -15 \text{ V} \dots 15 \text{ V}$		1.8		μC	
Input capacitance	C_{ies}	$V_{CE} = 25 \text{ V}$, $V_{GE} = 0 \text{ V}$, $f = 1 \text{ MHz}$, $T_{vj} = 25 \text{ }^{\circ}\text{C}$		25.2		nF	
Output capacitance	C_{oes}			2.1		nF	
Reverse transfer capacitance	C_{res}			0.64		nF	
Turn-on delay time	$t_{d(on)}$	$V_{CC} = 1800 \text{ V}$, $I_C = 250 \text{ A}$, $R_G = 10 \text{ }\Omega$, $C_{GE} = 0 \text{ nF}$, $V_{GE} = \pm 15 \text{ V}$, $L_{\sigma} = 400 \text{ nH}$, inductive load	$T_{vj} = 25 \text{ }^{\circ}\text{C}$		445		ns
			$T_{vj} = 125 \text{ }^{\circ}\text{C}$		450		ns
			$T_{vj} = 150 \text{ }^{\circ}\text{C}$		455		ns
Rise time	t_r		$T_{vj} = 25 \text{ }^{\circ}\text{C}$		195		ns
			$T_{vj} = 125 \text{ }^{\circ}\text{C}$		200		ns
			$T_{vj} = 150 \text{ }^{\circ}\text{C}$		210		ns
Turn-off delay time	$t_{d(off)}$	$V_{CC} = 1800 \text{ V}$, $I_C = 250 \text{ A}$, $R_G = 10 \text{ }\Omega$, $C_{GE} = 0 \text{ nF}$, $V_{GE} = \pm 15 \text{ V}$, $L_{\sigma} = 400 \text{ nH}$, inductive load	$T_{vj} = 25 \text{ }^{\circ}\text{C}$		1160		ns
			$T_{vj} = 125 \text{ }^{\circ}\text{C}$		1330		ns
			$T_{vj} = 150 \text{ }^{\circ}\text{C}$		1370		ns
Fall time	t_f		$T_{vj} = 25 \text{ }^{\circ}\text{C}$		260		ns
			$T_{vj} = 125 \text{ }^{\circ}\text{C}$		330		ns
			$T_{vj} = 150 \text{ }^{\circ}\text{C}$		360		ns
Turn-on switching energy	E_{on}	$V_{CC} = 1800 \text{ V}$, $I_C = 250 \text{ A}$, $R_G = 10 \text{ }\Omega$, $C_{GE} = 0 \text{ nF}$, $V_{GE} = \pm 15 \text{ V}$, $L_{\sigma} = 400 \text{ nH}$, inductive load	$T_{vj} = 25 \text{ }^{\circ}\text{C}$		360		mJ
			$T_{vj} = 125 \text{ }^{\circ}\text{C}$		460		mJ
			$T_{vj} = 150 \text{ }^{\circ}\text{C}$		515		mJ
Turn-off switching energy	E_{off}	$V_{CC} = 1800 \text{ V}$, $I_C = 250 \text{ A}$, $R_G = 10 \text{ }\Omega$, $C_{GE} = 0 \text{ nF}$, $V_{GE} = \pm 15 \text{ V}$, $L_{\sigma} = 400 \text{ nH}$, inductive load	$T_{vj} = 25 \text{ }^{\circ}\text{C}$		330		mJ
			$T_{vj} = 125 \text{ }^{\circ}\text{C}$		445		mJ
			$T_{vj} = 150 \text{ }^{\circ}\text{C}$		480		mJ
Short circuit current	I_{SC}	$t_{psc} \leq 10 \text{ }\mu\text{s}$, $V_{GE} = 15 \text{ V}$, $V_{CC} = 2500 \text{ V}$, $V_{CEM \text{ CHIP}} \leq 3300 \text{ V}$	$T_{vj} = 150 \text{ }^{\circ}\text{C}$	1090		A	

³⁾ Characteristic values according to IEC 60747 – 9

⁴⁾ Collector-emitter saturation voltage is given at chip level

Diode characteristic values ⁵⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Forward voltage ⁶⁾	V _F	I _F = 250 A	T _{vj} = 25 °C	2.05	2.5	V
			T _{vj} = 125 °C	2.25	2.6	V
			T _{vj} = 150 °C	2.2		V
Reverse recovery current	I _{rr}	V _{CC} = 1800 V, I _F = 250 A, V _{GE} = ±15 V, R _G = 10 Ω, C _{GE} = 0 nF, di/dt = 1 kA/μs L _σ = 400 nH, inductive load	T _{vj} = 25 °C	280		A
			T _{vj} = 125 °C	320		A
			T _{vj} = 150 °C	330		A
Recovered charge	Q _{rr}		T _{vj} = 25 °C	170		μC
			T _{vj} = 125 °C	270		μC
			T _{vj} = 150 °C	320		μC
Reverse recovery time	t _{rr}		T _{vj} = 25 °C	1160		ns
			T _{vj} = 125 °C	1580		ns
			T _{vj} = 150 °C	1830		ns
Reverse recovery energy	E _{rec}		T _{vj} = 25 °C	180		mJ
			T _{vj} = 125 °C	310		mJ
			T _{vj} = 150 °C	370		mJ

⁵⁾ Characteristic values according to IEC 60747 – 2

⁶⁾ Forward voltage is given at chip level

Package properties ⁷⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
IGBT thermal resistance junction to case	$R_{th(j-c)IGBT}$				0.051	K/W
Diode thermal resistance junction to case	$R_{th(j-c)DIODE}$				0.102	K/W
IGBT thermal resistance ²⁾ case to heatsink	$R_{th(c-s)IGBT}$	IGBT per switch, λ grease = 1W/m x K		0.048		K/W
Diode thermal resistance ²⁾ case to heatsink	$R_{th(c-s)DIODE}$	Diode per switch, λ grease = 1W/m x K		0.096		K/W
Comparative tracking index	CTI		> 600			
Module stray inductance	$L_\sigma \text{ CE}$			125		nH
Resistance, terminal-chip	$R_{CC'+EE'}$	per switch	$T_C = 25 \text{ °C}$	0.25		m Ω
			$T_C = 125 \text{ °C}$	0.33		
			$T_C = 150 \text{ °C}$	0.35		

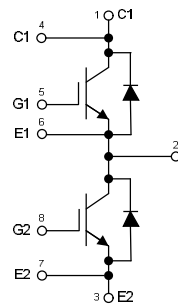
²⁾ For detailed mounting instructions refer to ABB Document No. 5SYA 2039

Mechanical properties ⁷⁾

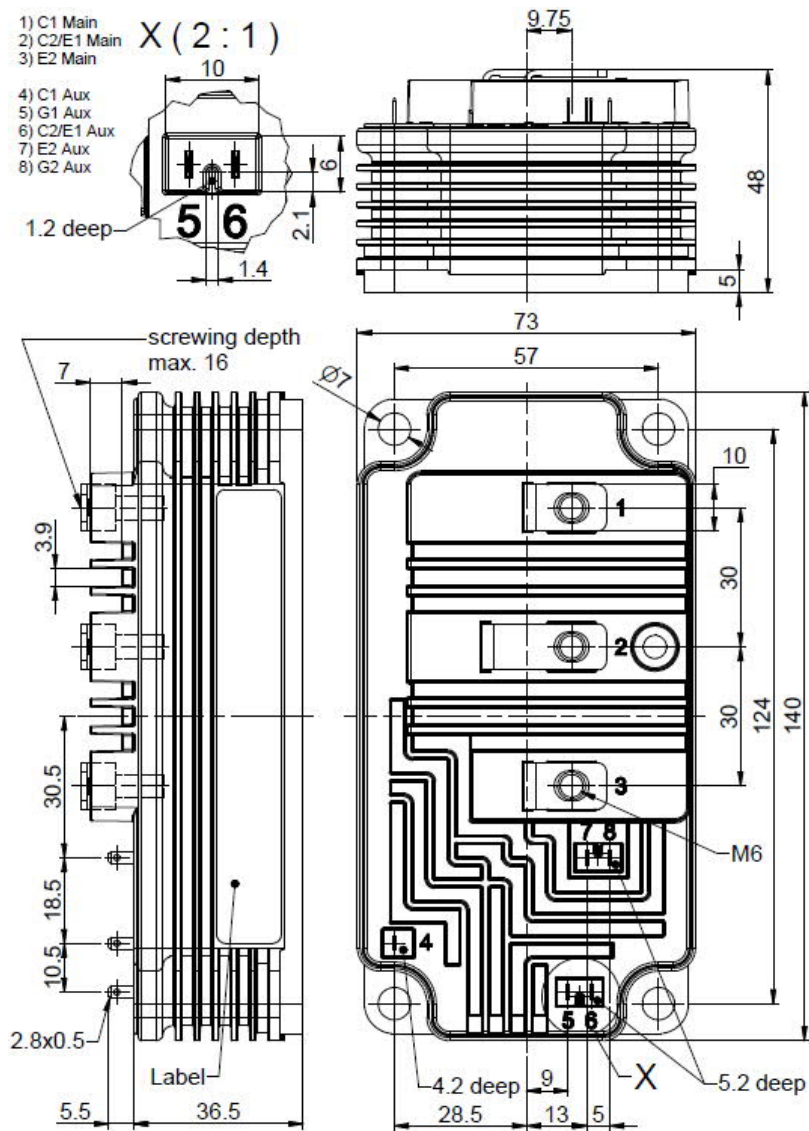
Parameter	Symbol	Conditions	min	typ	max	Unit
Dimensions	L x W x H	Typical	73 x 140 x 48			mm
Clearance distance in air	d_a	according to IEC 60664-1 and EN 50124-1	Term. to base:	35		mm
			Term. to term:	19		
Surface creepage distance	d_s	according to IEC 60664-1 and EN 50124-1	Term. to base:	64		mm
			C1 to E1:	54		
			C1 to E2:	78		
Mass	m			460		g

⁷⁾ Package and mechanical properties according to IEC 60747 – 15

Electrical configuration



Outline drawing ²⁾



Note: all dimensions are shown in millimeters

²⁾ For detailed mounting instructions refer to Document No. 5SYA 2039

This is an electrostatic sensitive device, please observe the international standard IEC 60747-1, chap. IX. This product has been designed and qualified for Industrial Level.

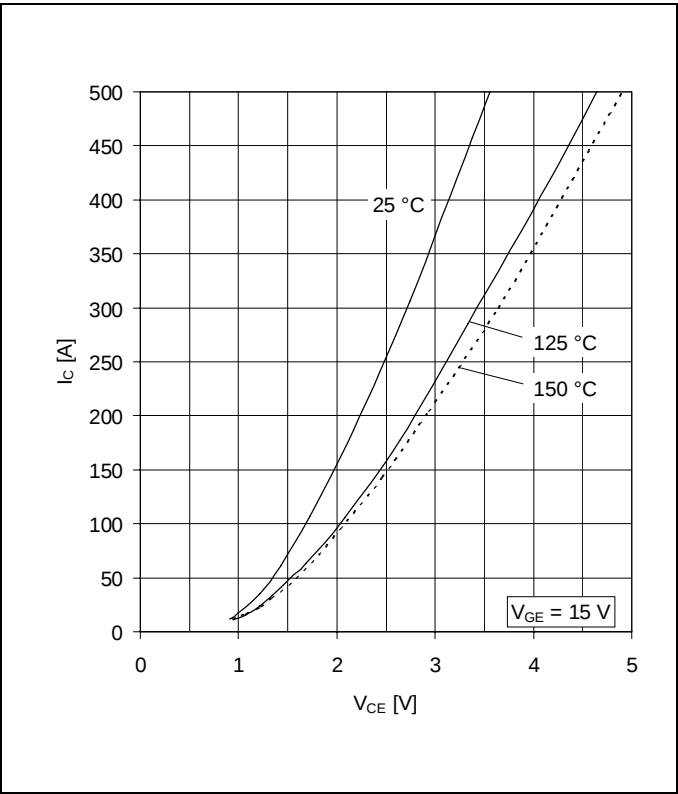


Fig. 1 Typical on-state characteristics, chip level

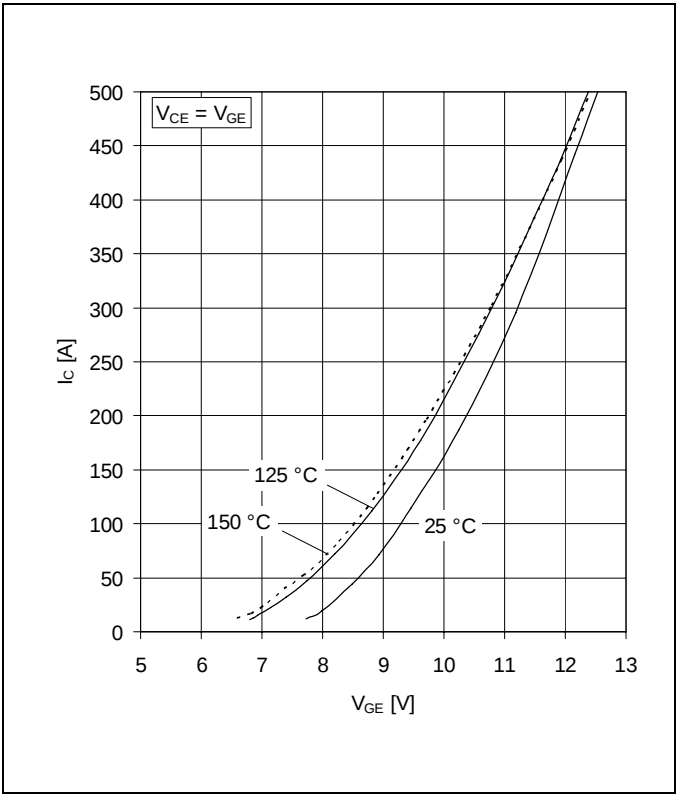


Fig. 2 Typical transfer characteristics, chip level

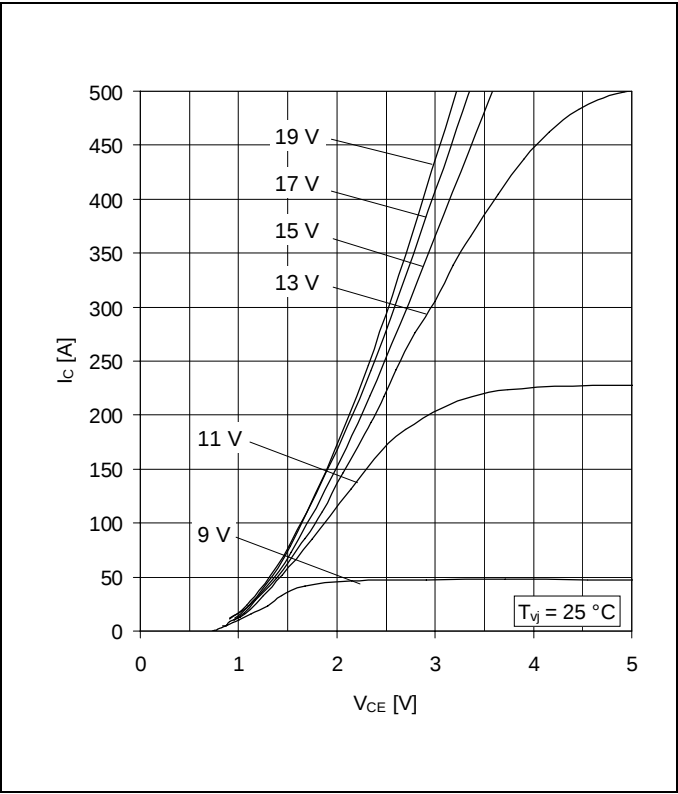


Fig. 3 Typical output characteristics, chip level

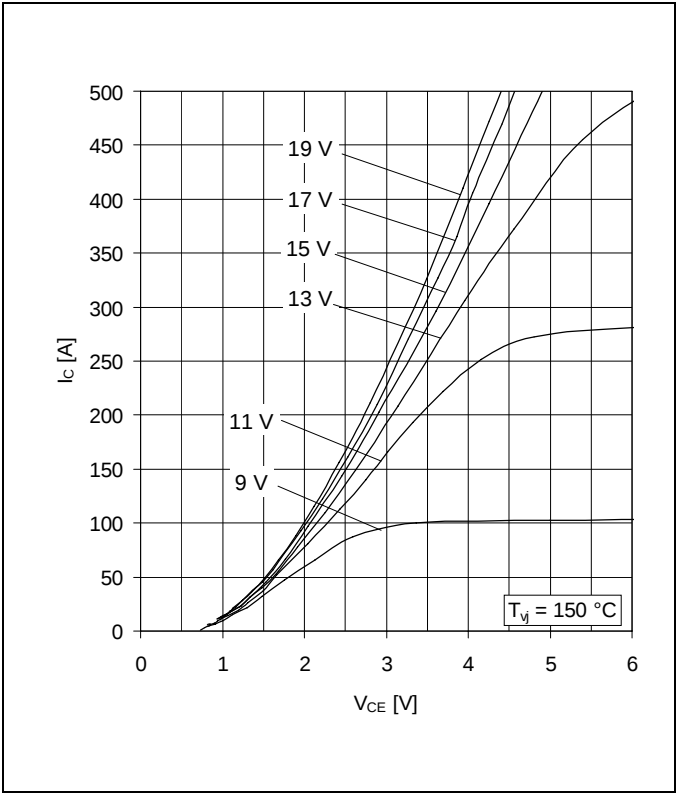


Fig. 4 Typical output characteristics, chip level

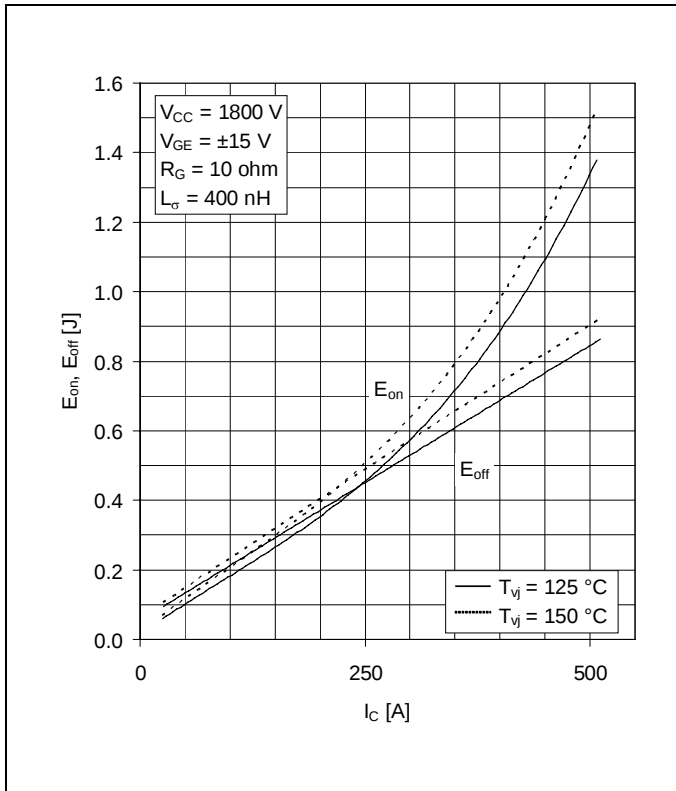


Fig. 5 Typical switching energies per pulse vs. collector current

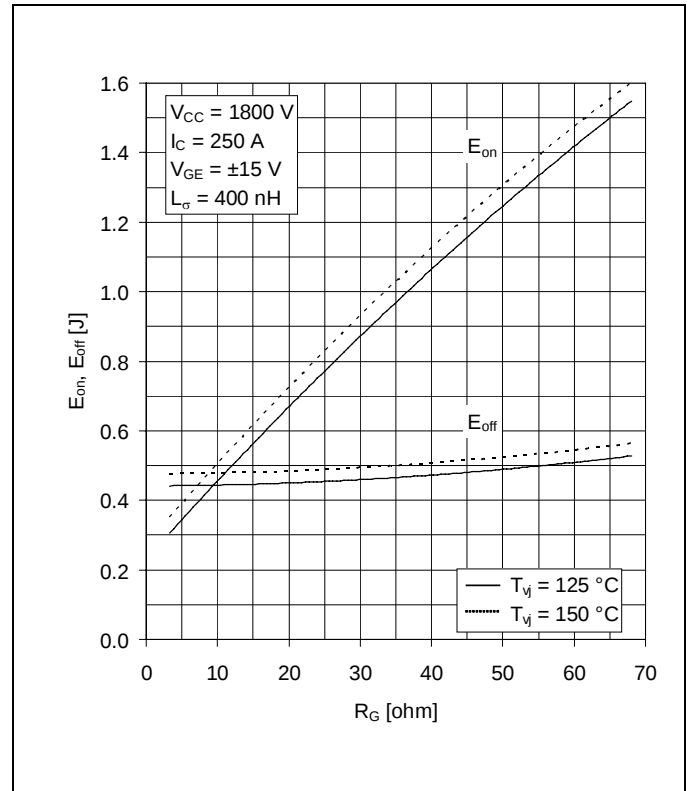


Fig. 6 Typical switching energies per pulse vs. gate resistor

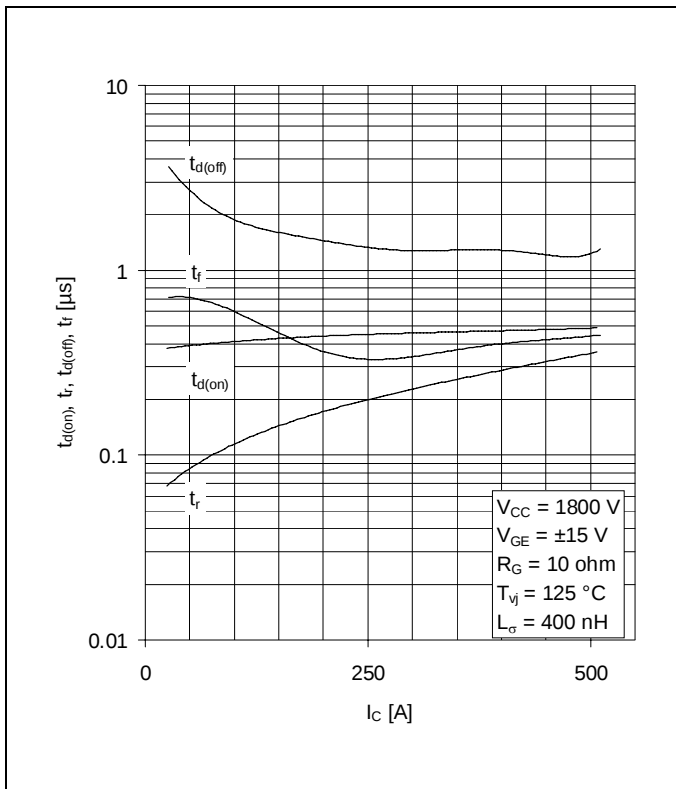


Fig. 7 Typical switching times vs. collector current

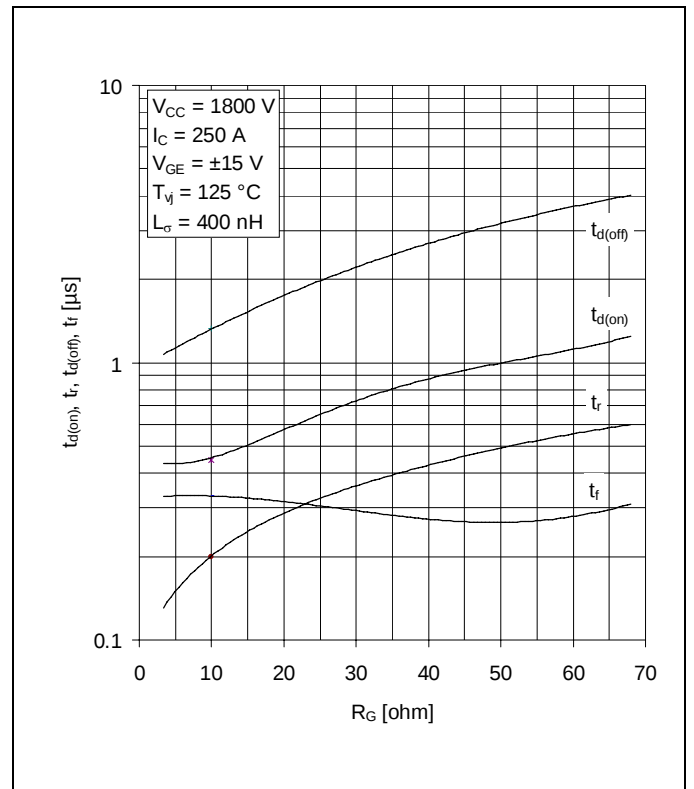


Fig. 8 Typical switching times vs. gate resistor

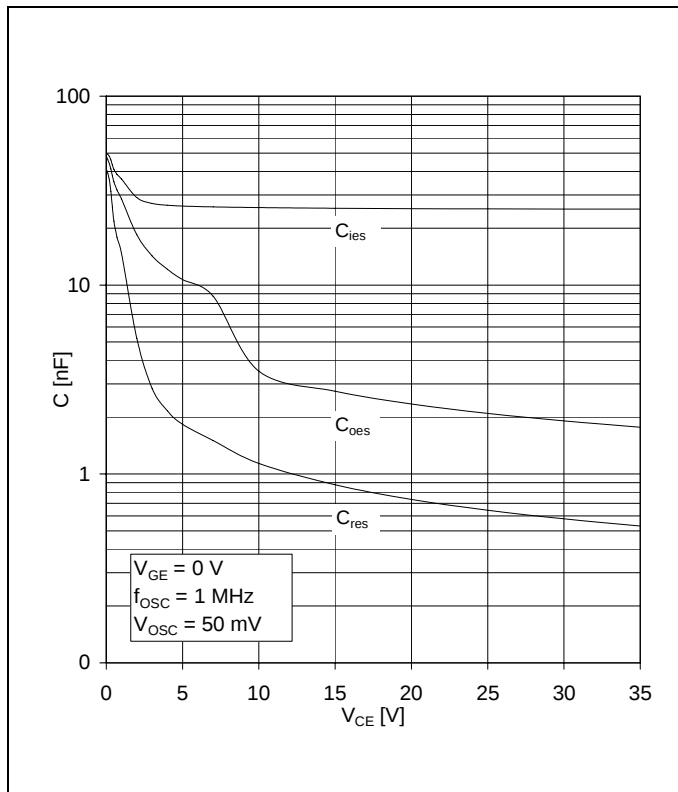


Fig. 9 Typical capacitances vs. collector-emitter voltage

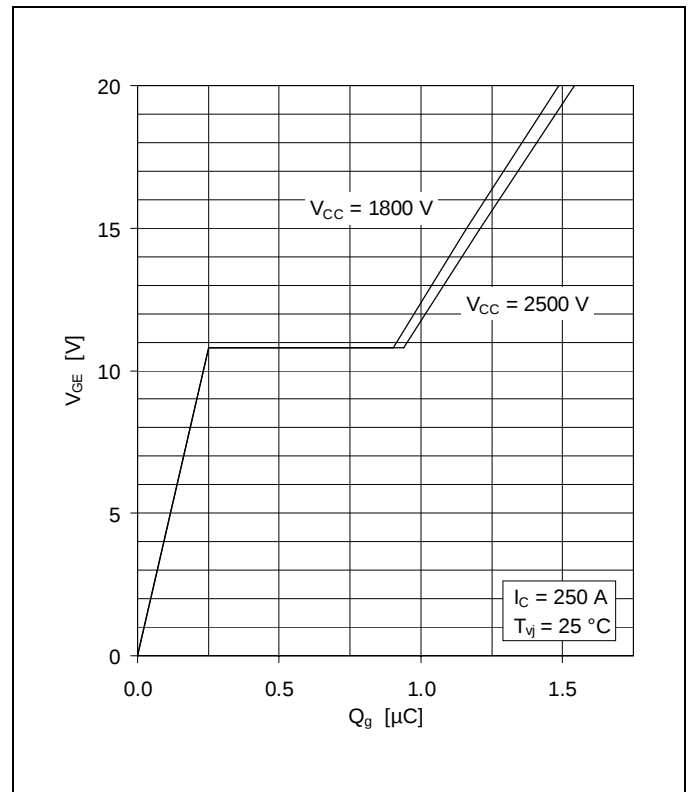


Fig. 10 Typical gate charge characteristics

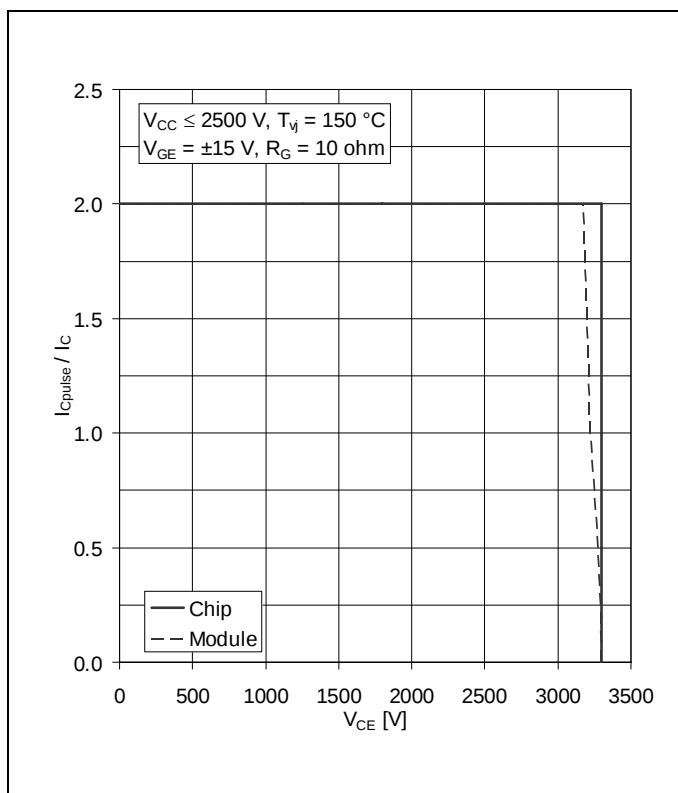


Fig. 11 Turn-off safe operating area (RBSOA)

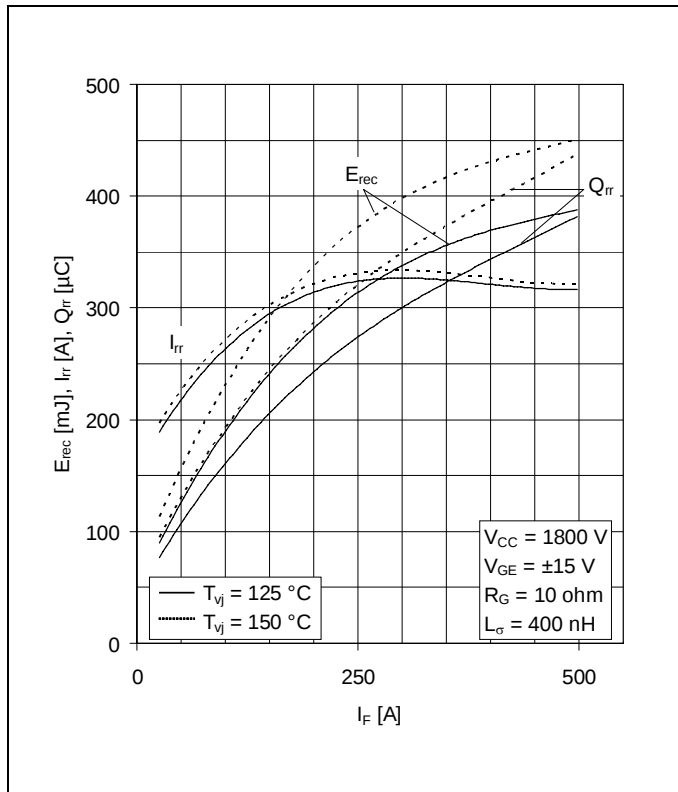


Fig. 12 Typical reverse recovery characteristics vs. forward current

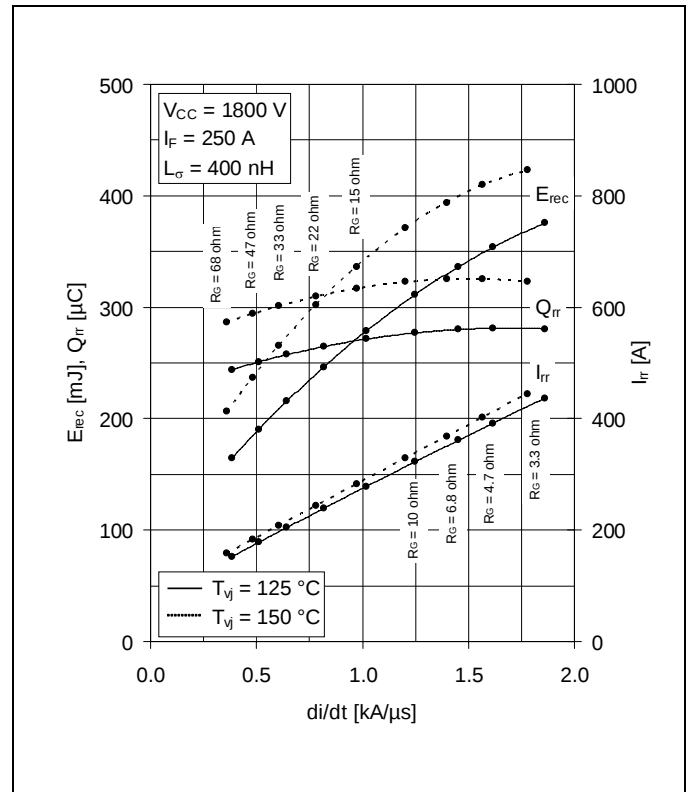


Fig. 13 Typical reverse recovery characteristics vs. di/dt

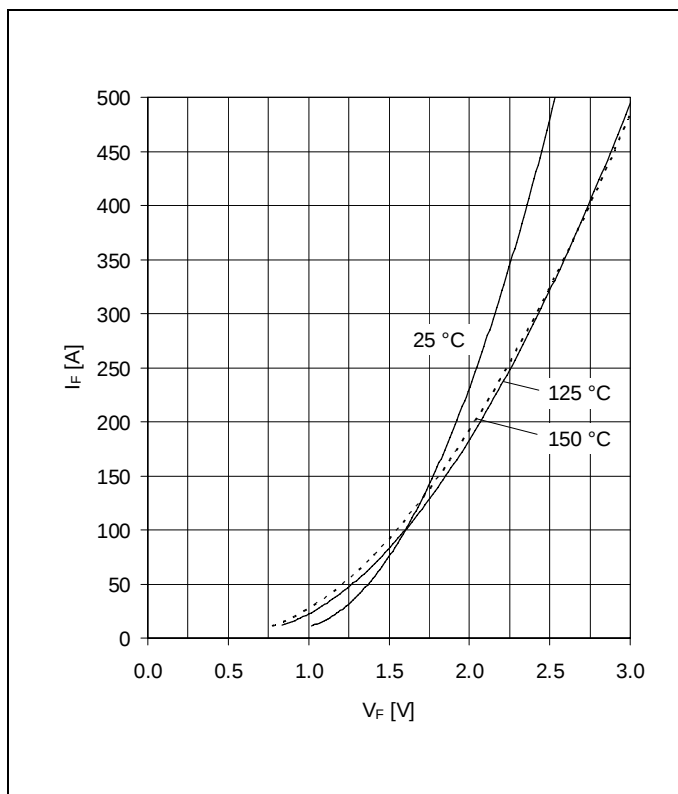


Fig. 14 Typical diode forward characteristics chip level

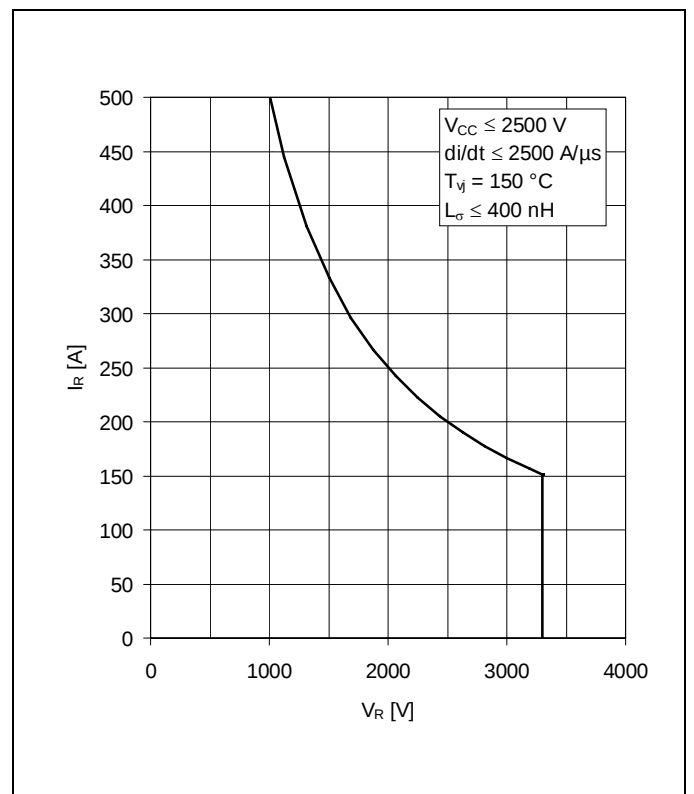


Fig. 15 Safe operating area diode (SOA)

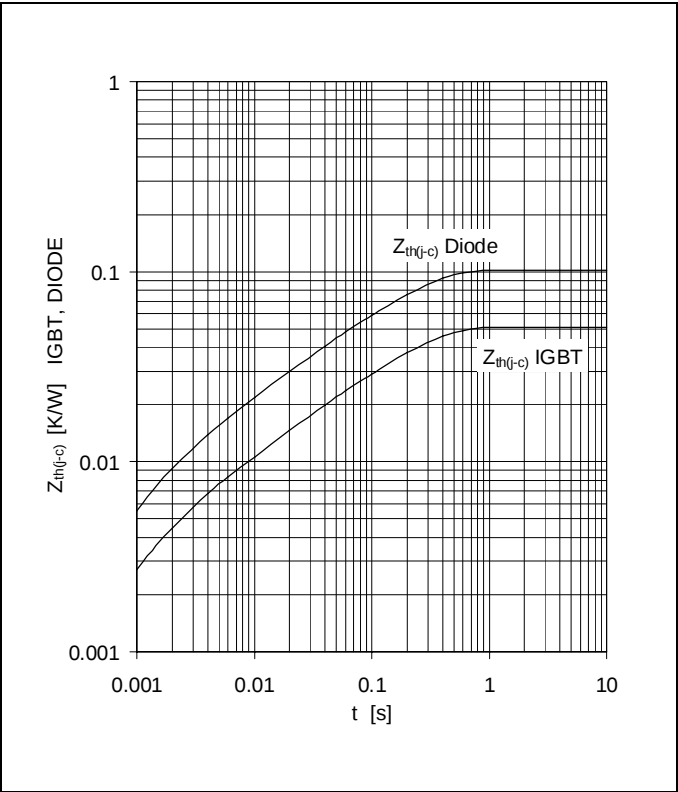


Fig. 16 Thermal impedance vs. time

Analytical function for transient thermal impedance:

$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i (1 - e^{-t/\tau_i})$$

	i	1	2	3	4	5
IGBT	Ri(K/kW)	35.1	8.25	3.84	3.79	
	τi(ms)	207.4	30.1	7.6	1.6	
DIODE	Ri(K/kW)	69.2	17.3	7.37	7.77	
	τi(ms)	203.6	30.1	7.5	1.6	

Related documents:

- 5SYA 2042 Failure rates of HiPak modules due to cosmic rays
- 5SYA 2043 Load – cycle capability of HiPaks
- 5SYA 2045 Thermal runaway during blocking
- 5SYA 2053 Applying IGBT
- 5SYA 2058 Surge currents for IGBT diodes
- 5SYA 2093 Thermal design of IGBT modules
- 5SYA 2098 Paralleling of IGBT modules
- 5SZK 9111 Specification of environmental class for HiPak Storage
- 5SZK 9112 Specification of environmental class for HiPak Transportation
- 5SZK 9113 Specification of environmental class for HiPak Operation (Industry)
- 5SZK 9120 Specification of environmental class for HiPak

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